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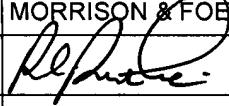
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		Application Number	10/720,378
		Filing Date	November 25, 2003
		First Named Inventor	Nobuyuki SEKIKAWA
		Art Unit	2814
		Examiner Name	S. Rao
Total Number of Pages in This Submission	4	Attorney Docket Number	492322001810

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	MORRISON & FOERSTER LLP		
Signature			
Printed name	Barry E. Bretschneider		
Date	May 24, 2005	Reg. No.	28,055



PATENT
Docket No. 492322001810

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Nobuyuki SEKIKAWA *et al.*

Serial No.: 10/720,378

Filing Date: November 25, 2003

For: METHOD OF MANUFACTURING
INSULATED GATE
SEMICONDUCTOR DEVICE

Examiner: S. Rao

Group Art Unit: 2814

RESPONSE UNDER 37 CFR 1.111

M/S Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Action mailed February 24, 2005, please reconsider this application in light of following remarks:

REMARKS

Claims 6-11 have been rejected under 35 USC 103(a) as unpatentable over U.S. Patent No. 5,397,909 (Moslehi) in view of U. S. Patent No. 5,882,994 (Araki). Applicants respectfully traverse this rejection.

Claim 6 recites forming a field oxidation film and a second gate oxide film through selective oxidation by using the oxidation protection film as a mask. The Examiner contends that Moslehi's interim gate etch-stop layer 52 corresponds to the claimed oxidation protection film and Moslehi's field insulating region 42 to the claimed field oxidation film and concludes

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